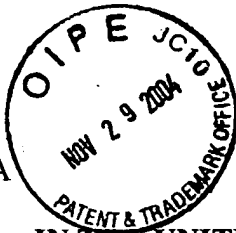


APLUS.001A



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Songjie Xu
Appl. No. : 09/754,406
Filed : January 2, 2001
For : METHODOLOGY AND
APPLICATIONS OF TIMING-
DRIVEN LOGIC RESYNTHESIS
FOR VLSI CIRCUITS
Examiner : Mary C. Hogan
Group Art Unit : 2123

CERTIFICATE OF MAILING

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

November 24, 2004

(Date)

Raimond J. Salenieks, Reg. No. 37,924

RESPONSE TO FIRST OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RECEIVED

DEC 02 2004

Technology Center 2100

Dear Sir:

In response to the Office Action dated May 26, 2004 (Paper No. 6) in the above-referenced patent application, please make the following amendments:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 12 of this paper.

Amendments to the Drawings begin on page 19 of this paper. A "Replacement Sheet" for each sheet of drawings being amended can be found in the Appendix.

Remarks/Arguments begin on page 20 of this paper.